

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	23	(hardware and software and (cosimulat\$4 or co-simulat\$4 or con with simulat\$4)).ti.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:14
L2	36	(hardware and software and (cosimulat\$4 or co-simulat\$4 or con with simulat\$4)).ab.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 13:02
L3	1	2 and (HLMS or (high adj level adj model\$4 adj system))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 13:03
L4	1265	(HLMS or (high adj level adj model\$4 adj system))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 13:04

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L5	8	4 and (cosimulat\$4 or co-simulat\$4 or con with simulat\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 13:06
L6	2	5 and (simulat\$4 with (block\$2 or group\$2)) and transfer\$5 and vector\$2	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 13:09
L7	1168	(703/14).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/03/14 13:07
L9	1045	(703/13).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/03/14 13:07

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L10	2	6 and invok\$4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 13:20
L11	2	10 and (PLA or PLG or PLD or FPGA)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:17
L12	5	5 and (PLA or PLG or PLD or FPGA)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 13:10
L13	3	5 and invok\$4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 13:16

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L14	1	5 and ((first with second) with block\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:17
L15	5	5 and ((simulat or co-simulat\$4) with block\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 13:15
L21	17	((vector\$2 with transfer\$4) and (cosimulat\$4 or co-simulat\$4 or con with simulat\$4))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 13:19
L22	253	((data with transfer\$4) and (cosimulat\$4 or co-simulat\$4 or con with simulat\$4))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 13:19

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L24	2	10 and (transfer\$4)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 13:20
L26	18	semulation	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:13
L28	16	(26 and (cosimulat\$4 or co-simulat\$4 or con with simulat\$4))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:14
L30	15	28 and (transfer\$4 and vector\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:15

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L31	16	28 and (transfer\$4 with data)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:16
L32	15	31 and vector\$2	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:16
L33	15	32 and (PLA or PLG or PLD or FPGA)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:17
L34	14	33 and ((first with second) with block\$2)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:26

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L40	9637	(burst with transfer\$5)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:28
L42	1	(burst with transfer\$5) and 4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:28
L43	25	(burst with transfer\$5) and 22	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:29
L44	23	(burst adj transfer\$5) and 22	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:34

EAST Search History

L45	11	34 and (data with frame)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/03/14 15:41
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